

DESIGN PANEL NO. 47 2-13-98

OCR PERIPHERAL REQUIREMENTS HWCI - Al Yust

TOPICS

- Tally of Peripherals Requirements survey responses
- Acquisition strategy and schedule for the installation of peripheral devices
- Specific peripheral device requirements
- A proposal of what and where peripherals could be installed

ACTIONS

	<u>ACTIONEE</u>	<u>DUE DATE</u>	<u>STATUS</u>
1. Provide the Ops concept of the following Items: <ul style="list-style-type: none">• Color Printers• Black and White Laser Printers• Photo Quality Color Printers• Fax Machines• Scanners• Strip Chart Recorders/X - Y Plotters with respect to operations in OCR including qty.	Steve Altemus	3/12/98	In work
2. Levy the appropriate requirements for incorporation of X - Y Plotter/Strip Chart analysis capability to the IPTs. Provide a method/plan of testing and evaluating this capability.	Rich Ikerd	3/12/98	In work
3. Revisit the shared IO area with the objective of reducing capability and cost of the items in that area.	Greg Clements	3/12/98	In work

Approved

DESIGN PANEL NO. 47 2-13-98

SHUTTLE DATA CENTER - CDS RE-PLATFORM - Earl Johnson

DESCRIPTION

This delivery is to provide continued use of the Shuttle Data Center as a replacement for CDS.

ACTIONS

ACTIONEE **DUE DATE** **STATUS**

No action required

Approved

DESIGN PANEL NO. 47 2-13-98

SIMULATION REHOST - Scott Estes

DESCRIPTION

The Simulation Rehost effort is part of the Central Data Subsystem (CDS) rehost of all applications from the Honeywell DPS-90 to a non-proprietary platform. Specifically, the Simulation Rehos is the functional replacement for the current simulation system that is based on the Shuttle Ground Simulator (SGOS). As currently scheduled, Atlas coincides with completion of the Simulation Rehost formal validation period and start of modifications to the operational VSI for transition to the new system.

ACTIONS

	<u>ACTIONEE</u>	<u>DUE DATE</u>	<u>STATUS</u>
1. The Hardware architecture for the connectivity of RTPS and or BASIS to the Simulation Rehost for mode 1 control needs to be defined and captured on the CLCS Hardware Block Diagrams. This action's resolution should be coordinated with the H/W Architecture Team.	Scott Estes	3/31/98	In work
2. An activity to validate proper connectivity between VSI and the HMF needs to be complete.	Scott Estes	3/15/98	In work

Approved